G H PATEL COLLEGE OF ENGINEERING & TECHNOLOGY
The Charutar Vidya Mandal (CVM) University
Vallabh Vidyanagar – 388 120
Department of Electronics and Communication Engineering



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Report on Electromanthan 2026

12th October 2026

Introduction

Electromanthan 2026, Electronics + Manthan | "Soch Se Circuit Tak" a project exhibition and competition, was held on 12 October 2026 at G H Patel College of Engineering and Technology (GCET), CVM University. The Department of Electronics and Communication Engineering organised it to give students a platform to turn classroom theory into working hardware. The event brought together more than 100 students who presented their own projects to faculty and an external jury.

Event Details

Particulars	Details
Event name	Electromanthan 2026 Electronics + Manthan | "Soch Se Circuit Tak"
Date	12 October 2026
Venue	Electronics Laboratory, GCET, CVM University
Organising department	Electronics and Communication Engineering
Faculty coordinator	Dr. Kavindra Jain (TR0600181) Dr Ajit Ningambom
Subject focus	Electronic Devices and Circuits
Participants	170+ students
Teams	80
Evaluation	Faculty coordinator and an external faculty jury member

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Participants of Electromanthan with faculty members in the Electronics Laboratory

Objectives

The event aimed to strengthen practical understanding of the Electronic Devices and Circuits course. Its main objectives were:

- To help students apply theoretical concepts such as diodes, transistors, amplifiers and switching circuits in real hardware.
- To build skills in circuit design, component selection, soldering, testing and troubleshooting.
- To encourage teamwork, creativity and problem-solving through hands-on project work.
- To improve students' technical communication by presenting and defending their work before a jury.

Projects Showcased

Over 80 teams displayed working projects built on concepts from the Electronic Devices and Circuits course. Each team set up its project in the Electronics Laboratory and demonstrated its operation live.

The projects covered a wide range of course topics, including rectifier and regulated power supply circuits, transistor-based switching and amplifier circuits, LED driver and indicator circuits, and sensor-based circuits for automation and safety. Several teams also combined discrete devices with digital logic ICs to build complete functional systems.

Students explained the working principle, circuit diagram and design choices behind their projects. Many teams designed their own circuit layouts and assembled them on breadboards, general-purpose boards or PCBs, reflecting a strong practical effort.



Teams setting up and testing their projects



A team demonstrating circuit output on an oscilloscope

Evaluation and Jury

The event was coordinated by **Dr. Kavindra Jain**, who guided the students throughout their project preparation. To keep the evaluation fair and objective, an external faculty member was invited as a jury member.

The jury visited every team and assessed the projects on technical understanding, innovation, circuit design, quality of implementation, working demonstration and presentation. The jury also questioned students on the concepts used and offered practical suggestions to improve their designs.



Students explaining their project to the evaluator



Project evaluation and discussion with faculty



Students presenting their work during evaluation

Outcomes and Learning

Electromanthan 2026, Electronics + Manthan | "Soch Se Circuit Tak" gave students valuable hands-on exposure beyond regular laboratory sessions. Through the event, students:

- Gained a deeper, practical understanding of electronic devices and circuit behaviour.
- Developed skills in testing, debugging and solving real hardware problems.
- Learned to work effectively in teams and manage a project from idea to working model.
- Improved their confidence in presenting technical work and answering expert questions.
- Received direct feedback from experienced faculty, helping them refine their designs for future work.

Conclusion

Electromanthan 2026, Electronics + Manthan | "Soch Se Circuit Tak" was conducted successfully, with enthusiastic participation from more than 100 students across over 50 teams. The event achieved its goal of linking the Electronic Devices and Circuits course with practical, real-world applications, and it encouraged a culture of innovation within the department.

The Department of Electronics and Communication Engineering thanks Dr. Kavindra Jain and Dr Ajit Ningambom for coordinating the event, the external jury member for their time and expert evaluation, the laboratory staff for their support, and all participating students for their effort and enthusiasm. The department looks forward to organising more such events in the future.