

Brief Report about ISSP - 2017

Department of Electronics & Communication Engineering, Information Technology and Electrical Engineering have jointly successfully organized International Conference on Intelligent Systems and Signal Processing during 24th and 25th March 2017.

The conference which was continuation of the legacy established by the department in the year 2009 and meant for offering a common platform for knowledge sharing, got an overwhelming response in the form of a huge number of academicians, researchers and industry personnel participation from across the country.

The main emphasis of this conference was on dissemination of information, experience and research results on the current topics of interest through in-depth discussions and participation of researchers from all over world. The objective was to provide a platform to the scientists, research scholars and industrialist to interact and exchange of ideas in a number of research areas. This would facilitate communication among researchers in different fields of Electrical Engineering, Electronics & Communication Engineering as well as Information Technology.

ISSP-2017 is facilitated with Pre-Conference Workshops on 23rd March 2017. Pre-Conference Workshops aimed to discuss theoretical and practical aspects of emerging technologies by renowned technocrats in the field of Communication & Information Technology and Electrical Engineering.

The conference was graced by eminent personalities like Mr Joseph Klein (CEO, Disrupt6 Inc.), Dr. S V Kulkarni (IIT, Bombay), Dr. Nischal Verma (IIT, Kanpur) and Dr. Himanshu Bahirat (IIT, Bombay) who delivered keynote sessions on insightful issues related to academics and industry. Delegates from different parts of the country presented their research papers. The delegates from various reputed academic institutions, and industry presented their research work. Many students also participated in the conference.

Dr. Himashu Soni (Principal, GCET) congratulated Dr. Bhaskar Thakkar (EC Head), Dr. Nikhil Gondaliya (IT Head), Dr. Ritesh Patel (EE Head) and all staff members with student volunteers for the success of the event. The departments have expressed deep sense of gratitude towards Charutar Vidya Mandal for supporting the conference.

4 ISSP - 2017 is technically Supported by: Springer Signal Processing Society

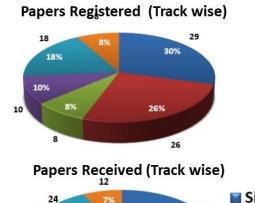
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4 Conference at a glance:

Conference at a Glance							
Pre-Conference Workshop	Торіс	Key No	ote Speakers	Technical Sponsors			
Mr. Sajad Rathor (Texas Instruments)	Internet of Things using ARM Cortex M4 Tiva MCU	Joseph Klein (CEO, Disrupt6 Inc. Virginia, USA) Prof. S V Kulkarni (IIT, Bombay)		Charutar Vidya Mandal (CVM)			
Prof. Himanshu Bahirat (IIT, Bombay)	Smart Grid Applications			Springer AISC Series ICTACT Journals			
Ms. Alka Nair (Mathworks, Pune, India)	MATLAB Demonstration			IEEE Signal Processing Society			
Collaboration with	Prof. Nishchal K. Verma (IIT, Kanpur)		Institutions of Engineers (India)				
nals. Industrial exhibition by Matlab(Mathworks), Tekctronix, Scientech, Edutech, Atlanta Trans- formers. Pre conference workshops on 23 March 2017			Indian Society for Tech- nical Education				
		Track 1	Signal Processing & Communication Engineering				
		Track 2 Track 3	Applied Electronics and Emerging Technologies Computer Vision and Machine Learning				
Paper Presentations 24 –25 March 2017		Track 4 Track 5	Big Data and Cloud Computing Power Electronics and Power Systems				
		Track 6		ol and Electrical Drives			



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Signal Processing & Communication Engineering
Applied Electronics and Emerging Technologies
Computer Vision and Machine Learning
Big Data and Cloud Computing
Power Electronics and Power Systems
Automation, Control and Electrical Drives

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A CHARUTAR VIDYA MA	2 TECHNOLO	DGY 24 - 25		AT, INDIA
			nce on Intelligent Systems	AND SIGNAL PROCESSING
🖊 Co	onference	ce Schedule		
Ģ	र्म GCET) अमिति विश्वसुखाय	~>	ollege of Engineering & Technology, Vallabh V A Charutar Vidya Mandal Institution al Conference on Intelligent Systems & Signal (ISSP-2017)	ISSP
	ay I	Time	Event Description	Venue
F	riday	09:00 - 10:00 AM 10:00 - 11:00 AM	Registration and High Tea	GCET Foyer & Auditorium Front Lawn Smt. Kamlaben Shankarbhai Patel Auditorium (GCET)
	017	11:00 - 12:30 PM	Key Note Talk I - Mr. Joseph Klein,CEO, Disrupt6 Inc. Virginia,USA Topic: From Ball Bearing to Smart Cities - Innovation That Change the World	Auditorium (GCET)
	R	12:30 - 02:00 PM	LUNCH	Auditorium Front Lawn
	March,	02:00 - 03:30 PM	Key Note Talk II - Dr. S V Kulkarni Professor, Department of Electrical Engineering, IIT Bombay Topic: An Overview of Application & Research Opportunities in Electromagnetics	Auditorium (GCET)/ Seminar Hall
	<u>a</u>	03:15 - 03:30 PM	Tea Break	Auditorium Front Lawn
	Σ	03:30 - 05:30 PM	Paper Presentation Session 1	
	_		Track 1: Signal Processing & Communication Engineering	Seminar Hall
	4		Track 2: Applied Electronics and Emerging Technologies	EC AV ROOM - A125
	24 th		Track 5: Power Electronics and Power Systems Track 6: Automation, Control and Electrical Drives	Power Electronics Lab - D104 GFL 1
		06:30 PM Onwards	Musical Evening & Gala Dinner	Auditorium Front Lawn

G H Patel College of Engineering & Technology, Vallabh Vidhyanagar. A Charutar Vidya Mandal Institution

GCET

International Conference on Intelligent Systems & Signal Processing

Day 2	(ISSP-2017)					
Duy 2	Time	Event Description	Venue			
Saturday	09:00 - 09:30 AM	High Tea	Auditorium Front Lawn			
	09:30 - 11.00 AM	Paper Presentation Session 2				
		Track 1: Signal Processing & Communication Engineering	Seminar Hall			
		Track 2: Applied Electronics and Emerging Technologies	EC AV ROOM - A125			
2		Track 3: Computer Vision and Machine Learning	GFL 1			
h, 2017	11:00 - 12:30 PM	Key Note Talk III - Dr. Nischal Verma Professor, Department of Electrical Engineering, Indian Institute of Technology, Kanpur Topic: Deep Learning & Computational Intelligence	Auditorium (GCET)			
March,	12:30 - 02:00 PM	LUNCH	Auditorium Front Lawn			
ភ	02:00 - 03:30 PM	Paper Presentation Session 3				
Σ		Track 1: Signal Processing & Communication Engineering	Seminar Hall			
		Track 2: Applied Electronics and Emerging Technologies	EC AV ROOM - A125			
2 [‡]		Track 4: Big Data and Cloud Computing	GFL 1			
LC L		Track 5: Power Electronics and Power Systems	Power Electronics Lab - D104			
2	03:30 - 04:00 PM	Tea Break	Auditorium Front Lawn			
	04:00 - 04:30 PM	Valedictory Function	Seminar Hall			

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GCET Foyer and Registration Desk



Unveiling of Book of Abstract & CD Proceedings

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Keynote Session: Joseph Klein



Keynote Session: Dr. S V Kulkarni

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Dr. C L Patel during inaugural speech



Inaugural function dignitaries on the dais

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Keynote session: Dr. Nischal Verma



Student Interaction with Joseph Klein

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Paper Presentation Session



Paper Presentation Session

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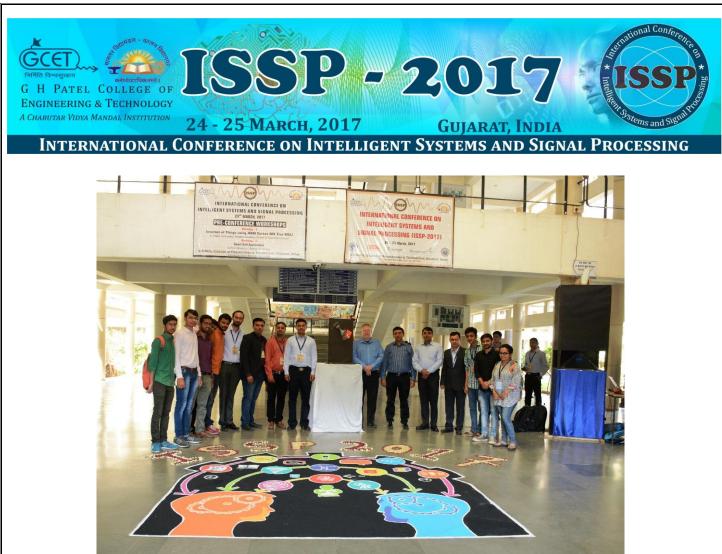


Paper Presentation Session



Certificate Distribution

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GCET Foyer



Group Photo

Thank you.

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